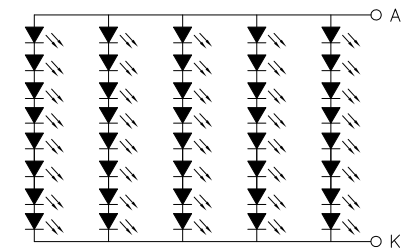


PIN ASSIGNMENT			
PIN NO.	SYMBOL	PIN NO.	SYMBOL
16	LCD-GND	1	NC
17	HPD	2	H-GND
18	BL-GND	3	LANE1-N
19	BL-GND	4	LANE1-P
20	BL-GND	5	H-GND
21	BL-GND	6	LANE0-N
22	BL-ENABLE	7	LANE0-P
23	BL-PWM-DIM	8	H-GND
24	NC	9	AUX-CH-P
25	NC	10	AUX-CH-N
26	BL-PWR	11	H-GND
27	BL-PWR	12	LCD-VDD
28	BL-PWR	13	LCD-VDD
29	BL-PWR	14	NC
30	NC	15	LCD-GND



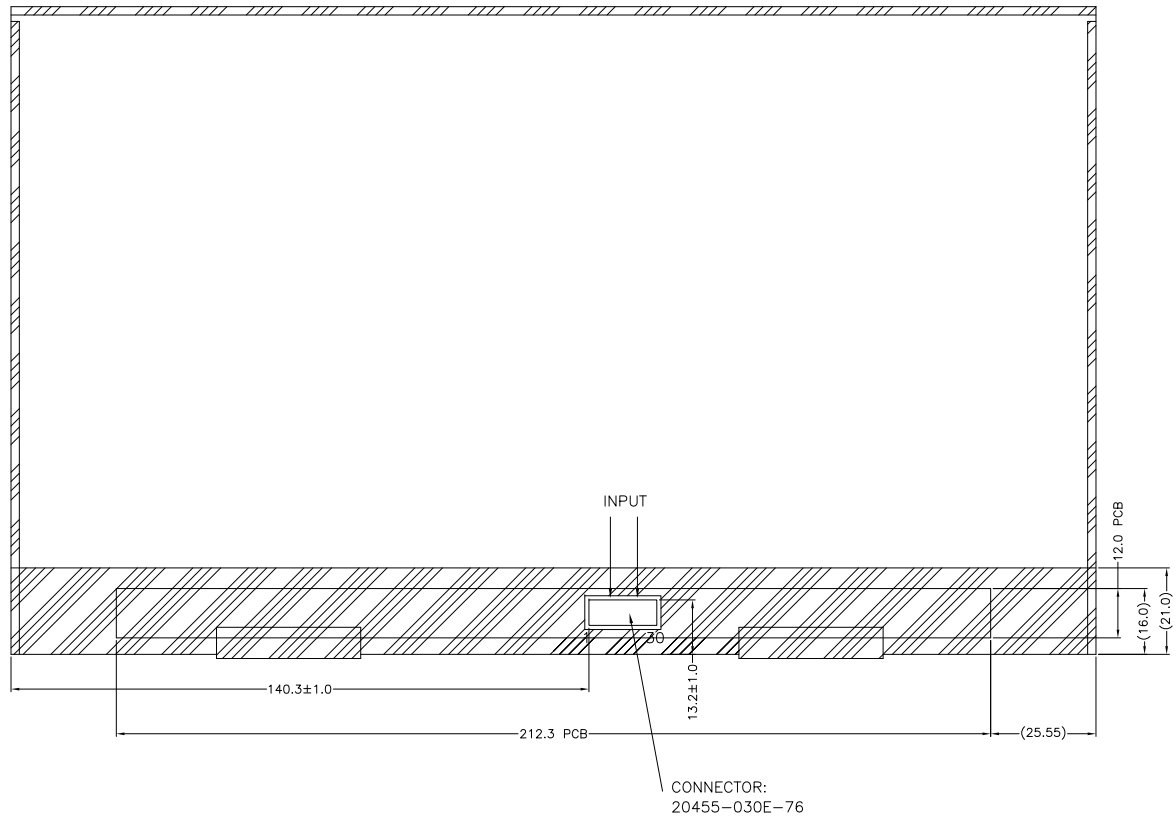
BACKLIGHT CIRCUIT DIAGRAM

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK.
2. OPERATING VOLTAGE: VDD=3.3V.
3. VIEWING DIRECTION: FULL VIEWING ANGLE.
4. IC DRIVER: TBD.
5. OPERATING TEMP: -10°C ~ 60°C.
6. STORAGE TEMP: -20°C ~ 70°C.
7. SURFACE LUMINANCE: 350CD/M²(TYP.).
8. LED BACKLIGHT: 40-CHIP WHITE LED, If=100mA, Vf=21.6V~27.2V.
9. SURFACE TREATMENT: ANTI-GLARE.
10. GENERAL TOLERANCE: ±0.3MM.
11. ROHS COMPLIANT.

03	UPDATE SURFACE LUMINANCE FROM 330NITS TO 350NITS.	RITA	2019.11.15
02	UPDATE SURFACE LUMINANCE FROM 250NITS TO 330NITS.	CAROL	2019.10.25
01	FIRST ISSUE	RITA	2019.10.09
VER.	REVISED DESCRIPTION	REVISER	DATE

MULTI-INNO TECHNOLOGY CO., LTD.		DRAWN BY: RITA 2019.10.09	SCALE : 1/1	MODULE P/N: MI1160AT-1
		CHECKED BY:	UNIT: mm	DESCRIPTION: TFT MODULE
VER. 03		SHEET NO.: 1/2		



MULTI-INNO TECHNOLOGY CO., LTD.

03	UPDATE SURFACE LUMINANCE FROM 330NITS TO 350NITS.	RITA	2019.11.15	DRAWN BY:	RITA	2019.10.09		MODULE P/N:
02	UPDATE SURFACE LUMINANCE FROM 250NITS TO 330NITS.	CAROL	2019.10.25	CHECKED BY:			SCALE : 1/1	MI1160AT-1
01	FIRST ISSUE	RITA	2019.10.09	APPROVED BY:			UNIT: mm	DESCRIPTION:
VER.	REVISED DESCRIPTION	REVISER	DATE	VERSION NO:	03		SHEET NO.: 2/2	TFT MODULE